

## Certificate of Facsimile Transmission

I hereby certify that the attached Issue Fee Transmittal with PTO Form 85B, Applicant's Comments on Examiner's Statement of Reasons For Allowance, PTO Form 2038 authorizing credit card payment of \$1,700.00 for the Issue (\$1,400.00) and Publication (\$300.00) fees are submitted to the U.S. Patent and Trademark Office via facsimile number (571) 273-2885 on the date shown below. (Total 4 pages).

Date: August 11, 2005

  
Li Mei Vermilya

PATENT APPLICATION  
Do. No. 4591-383  
Client No. IE10112-US-1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: You-Cheol SHIN, et al.

Serial No.: 10/777,233 Examiner: Brewster, William M.

Filed: February 11, 2004 Group Art Unit: 2823

For: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME INCLUDING FORMING METAL  
SILICIDE GATE LINES AND SOURCE LINES (as amended)TRANSMITTAL LETTER

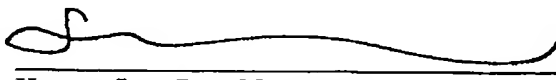
Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- ☒ Publication (\$300.00) and Issue (\$1400.00 large) fees
- ☒ In connection with issuance of a patent:
  - ☐ Supplemental Declaration ☒ PTO Form 85B
- ☒ Applicant's Comments on Examiner's Statement of Reasons For Allowance
- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.

  
Hosoon Lee, Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C.  
210 SW Morrison Street, Suite 400  
Portland, OR 97204  
503-222-3613

RECEIVED  
CENTRAL FAX CENTER

AUG 11 2005

PATENT APPLICATION

Do. No. 4591-383

Client No. IE10112-US-1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: You-Cheol SHIN, et al.

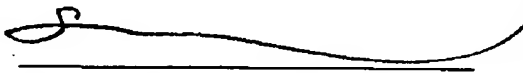
Serial No.: 10/777,233 Examiner: Brewster, William M.

Filed: February 11, 2004 Group Art Unit: 2823

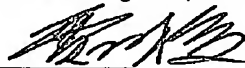
For: SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME INCLUDING FORMING METAL  
SILICIDE GATE LINES AND SOURCE LINES (as amended by  
Examiner)Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR  
ALLOWANCE

The Examiner's statement of reasons for Allowance lists some, but not all, of the reasons for allowance. The Examiner recited portions of an allowed independent claim and stated that the prior art did not teach or fairly suggest the recited portions. The applications note that it is a well known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

Customer No. 20575

Respectfully submitted,  
MARGER JOHNSON & McCOLLOM, P.C.Hosoon Lee  
Reg. No. 56,737MARGER JOHNSON & McCOLLOM, P.C.  
210 SW Morrison Street, Suite 400  
Portland, OR 97204  
503-222-3613

I hereby certify that this correspondence  
is being transmitted to the U.S. Patent and  
Trademark Office via facsimile number  
571-273-2885, on August 10, 2005.



Li Mei Vermilya